

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No. 10/082,599
 Filing Date February 22, 2002
 Inventor Craig M. Carpenter et al.
 Assignee Micron Technology, Inc.
 Group Art Unit 1763
 Examiner Karla A. Moore
 Attorney Docket No. MI22-1941
 Customer No. 021567
 Title: Interfacial Structure for Semiconductor Substrate Processing Chambers
 and Substrate Transfer Chambers and for Semiconductor Substrate
 Processing Chambers and Accessory Attachments, and Semiconductor
 Substrate Processor

GROUP 1700

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INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with
 37 CFR §1.56. Copies of the cited art are included. No admission is made
 regarding whether all the submitted references are prior art.

Respectfully submitted,

Dated: 10-28-03By: 

Mark S. Matkin
 Reg. No. 32,268

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Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-1941		SERIAL NO. 10/082,599	
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U.S. PATENT DOCUMENTS							
*Examiner's Initials	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA	4,948,979	08/1990	Munakata et al.			
	AB	5,076,205	12/1991	Vowles et al.			
	AC	5,364,219	11/1994	Takahashi et al.			
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FOREIGN PATENT DOCUMENTS							
Document Number	Date	Country	Class	Subclass	Translation		
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AC	10/365,085		Carpenter et al.			02/11/2003	
AD							
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